



EV077332168

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 09/921,518
Filing Date August 1, 2001
Inventorship John T. Moore
Assignee Micron Technology, Inc.
Group Art Unit 2818
Examiner Phuc T. Dang
Attorney's Docket No. MI22-1669
Title: Method of Forming Integrated Circuitry, Method of Forming Memory Circuitry, and Method of Forming Non-Volatile Random Access Memory Circuitry

7/Suppl
I.D.S.
G-866
8-22-02

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. Copies of the cited art are attached hereto. No admission is made regarding whether all the submitted references are prior art.

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By:

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	Filing Date	August 1, 2001
	First Named Inventor	John T. Moore
	Group Art Unit	2818
	Examiner Name	Phuc T. Dang
Total Number of Pages in This Submission	Attorney Docket Number MI22-1669	

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